

Jc4(920)

JC952 U.S. PRO  
09/00016  
G-100001

		Subclass	
	Class		ISSUE CLASSIFICATION

**PATENT NUMBER**

**U.S. UTILITY Patent Application**

 <b>J.D. Cade</b> Q.A. TM	<b>O.I.P.E.</b>	<b>PATENT DATE</b>
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APPLICATION NO. 09/838316	CONT/PRIOR F	CLASS 438	SUBCLASS 520	ART UNIT 5812 5813	EXAMINER chen
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TS Zhiheng Li

Method for fabricating silicon-on-insulator material

PTO-2040  
12/99

TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.				NOTICE OF ALLOWANCE MAILED	
	(Assistant Examiner) _____ (Date)				
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	(Primary Examiner) _____ (Date)			Amount Due	Date Paid
<input type="checkbox"/> The terminal ____ months of this patent have been disclaimed.				ISSUE BATCH NUMBER	
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